



Comparative Study of AI-Based Optimization Techniques in VLSI Circuit Design Using Python

¹Vasudha Patil

¹Assistant Professor, Department of Artificial Intelligence & Data Science,
Shri Chhatrapati Shivaji Maharaj College of Engineering, Nepti, Ahilyanagar, India.

Corresponding Author: vasudhapatil28@gmail.com

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ABSTRACT

Successful design of very large scale integration (VLSI) circuits requires the use of various optimizations—power reduction, area, and time optimization. This requires the involvement of AI-based optimization techniques, such as machine learning (ML), deep learning (DL), genetic algorithms (GA), particle swarm optimization (PSO), and reinforcement learning (RL), to fulfill such tasks where traditional approaches fail to do so. The abundant libraries and ease of implementation have made Python a widely used platform in AI-based VLSI optimization. The methodology used in this study is a comparative study of different AI-based optimization algorithms used in the VLSI circuits. This research is structured in the following way: problem definition, data collection & preprocessing, implementation on Python, and performance evaluation. The best number was obtained using DL (CNN) and RL (95%) for the complex tasks (floorplanning and dynamic power management). ML (SVM) achieved high accuracy (90%) on well-defined problems but was not scalable for large designs. GA and PSO were good in performing global optimizations for problems like area minimization and routing but required fine-tuning of parameters. DL and RL had the highest computational complexity (GPU acceleration) and the lowest resource requirements, with easier implementation, with ML techniques. We found that DL and RL are better suited to large and complex designs and are slow for small and simple designs, whereas ML (SVM) and PSO are better suited to small and simple designs and are often slow given large and complex designs. Therefore, additional research is needed on the use of multiple AI techniques in a hybrid way to overcome limitations of the different techniques and enhance design efficiency. The results of this study can be applied in the following areas: AI-based Electronic Design Automation (EDA) tools, Power and performance optimization in semiconductor industries, Automated circuit design for advanced computing and development of Python-based AI stacks for VLSI research.

Keywords: VLSI Optimization; AI-Based Design; Machine Learning; Electronic Design Automation (EDA).

I. INTRODUCTION

The field of Very Large Scale Integration (VLSI) circuit design has undergone significant transformations over the past few decades, driven by the relentless pursuit of higher performance, lower power consumption, and reduced chip area. As the complexity of integrated circuits (ICs) continues to grow, traditional design methodologies are increasingly challenged to meet the stringent requirements of modern applications such as artificial intelligence (AI), Internet of Things (IoT), and 5G communications [3]. The design of VLSI circuits involves multiple stages, including synthesis, placement, routing, and verification, each of which presents unique optimization challenges. Conventional optimization techniques, such as linear programming and heuristic algorithms, often struggle to handle the complexity and scale of modern VLSI designs, leading to suboptimal solutions and extended design cycles [1].

In recent years, AI-based optimization techniques have emerged as a powerful tool for addressing these challenges. Techniques such as machine learning (ML), deep learning (DL), genetic algorithms (GA), particle swarm optimization (PSO), and reinforcement learning (RL) have demonstrated significant potential in optimizing various aspects of VLSI design, including power consumption, area minimization, timing, and thermal management [5]. These AI-driven approaches leverage large datasets and computational power to identify optimal design solutions, often outperforming traditional methods in terms of accuracy and efficiency [11]. For instance,



ML-based models can predict power consumption and timing violations with high accuracy, enabling designers to make informed decisions early in the design process [7].

Python has become the programming language of choice for implementing AI-based optimization techniques in VLSI design due to its simplicity, extensive libraries, and strong support for AI frameworks such as TensorFlow, PyTorch, and Scikit-learn [13]. Python's versatility allows researchers and designers to rapidly prototype and test AI models, making it an ideal tool for exploring innovative optimization strategies. Furthermore, the open-source nature of Python-based tools has fostered collaboration and knowledge sharing within the VLSI community, accelerating the adoption of AI-driven design methodologies [17].

Despite the promising results achieved by AI-based optimization techniques, several challenges remain. The integration of AI with existing electronic design automation (EDA) tools is still in its early stages, and the lack of standardized benchmarks and datasets makes it difficult to compare the performance of different approaches [12]. Additionally, the computational complexity of AI models, particularly DL and RL, poses significant challenges for large-scale VLSI designs, necessitating the development of efficient algorithms and hardware acceleration techniques [4]. Addressing these challenges is critical to unlocking the full potential of AI-driven VLSI optimization and enabling the design of next-generation ICs.

The primary motivation for this research is to address the limitations of traditional optimization techniques in VLSI circuit design by leveraging the power of AI. The increasing complexity of modern ICs, coupled with the growing demand for low-power and high-performance devices, has created a pressing need for innovative optimization strategies that can deliver superior results in a timely manner [20]. AI-based techniques offer a promising solution to this problem, as they can handle large-scale design problems and identify optimal solutions with minimal human intervention [2].

The objectives of this research are threefold. First, we aim to conduct a comprehensive comparative study of AI-based optimization techniques in VLSI circuit design, evaluating their performance across various design stages and metrics. Second, we seek to develop a Python-based framework for implementing and testing these techniques, providing a flexible and scalable platform for experimentation. Third, we aim to identify research gaps and challenges in the field, offering insights into future directions for AI-driven VLSI optimization.

This paper makes several key contributions to the field of VLSI circuit design. First, it provides a detailed review of AI-based optimization techniques, highlighting their strengths and limitations in addressing specific design challenges. Second, it presents a Python-based framework for implementing these techniques, enabling researchers and designers to explore innovative optimization strategies. Third, it identifies critical research gaps and challenges, offering a roadmap for future research in AI-driven VLSI optimization. By addressing these issues, this paper aims to advance the state-of-the-art in VLSI design and contribute to the development of next-generation ICs.

II. LITERATURE REVIEW

The integration of Artificial Intelligence (AI) in Very Large Scale Integration (VLSI) circuit design has revolutionized the field by enabling efficient optimization techniques that address complex challenges such as power consumption, area minimization, timing, and thermal management. This literature review provides an in-depth analysis of AI-based optimization techniques in VLSI circuit design, focusing on their implementation using Python, and identifies research gaps that warrant further exploration.

A. AI-Based Optimization Techniques in VLSI Design

AI-based optimization techniques, including machine learning (ML), deep learning (DL), genetic algorithms (GA), particle swarm optimization (PSO), and reinforcement learning (RL), have been extensively studied for VLSI circuit design. Khailany et al. [3] conducted a comparative analysis highlighting their effectiveness in addressing power and area constraints in VLSI circuits. Their work demonstrated that ML-based approaches, particularly neural networks, outperform traditional optimization methods in terms of accuracy and computational efficiency. Similarly, Huang et al. [1] provided a comprehensive survey of ML approaches for VLSI optimization, emphasizing the role of Python-based frameworks in automating design processes. Python has emerged as a



preferred programming language for implementing AI-driven VLSI optimization tools due to its extensive libraries and ease of integration with ML frameworks. Lin et al. [5] developed a Python-based framework for AI-driven VLSI circuit optimization, showcasing its ability to streamline design automation and reduce development time. Their work highlighted the importance of open-source tools like TensorFlow and PyTorch in enabling rapid prototyping of AI models for VLSI applications. Ajayi and Blaauw [17] further reviewed Python-based AI tools for VLSI design automation, noting the growing adoption of DL frameworks like Keras and Scikit-learn in the VLSI community.

B. Comparative Analysis of AI Techniques

Several studies have compared the performance of different AI techniques in VLSI circuit optimization. Shanavas and Gnanamurthy [11] compared genetic algorithms and neural networks for VLSI circuit optimization, concluding that GAs are more suitable for global optimization problems, while neural networks excel in local optimization tasks. Wang et al. [7] explored AI-based techniques for power optimization in VLSI circuits, demonstrating that RL-based approaches achieve superior results compared to traditional methods. Their Python implementation provided a scalable solution for power-aware VLSI design. Liu et al. [12] compared PSO and simulated annealing in VLSI circuit design, showing that PSO is more effective for high-dimensional optimization problems. Deep learning has also gained traction in VLSI optimization, particularly for tasks such as floorplanning and placement. Devarasetty et al. [13] conducted a comparative study of DL techniques for VLSI circuit optimization, highlighting their ability to handle large-scale design problems efficiently. Their work emphasized the need for robust datasets and computational resources to train DL models effectively. Mirhoseini et al. [4] proposed an AI-driven framework for VLSI floorplanning using Python, achieving significant improvements in area and wirelength optimization. Agnesina et al. [20] explored RL-based approaches for VLSI circuit optimization, demonstrating their effectiveness in reducing design cycle times.

C. AI-Driven Optimization in Specific VLSI Design Tasks

AI techniques have been applied to various VLSI design tasks, including floorplanning, placement, routing, clock tree synthesis, and signal integrity optimization. Rapp et al. [2] conducted a comparative analysis of AI techniques for timing optimization in VLSI circuits, showing that ML-based approaches significantly reduce timing violations. Kahng [18] developed Python-based AI tools for VLSI placement and routing optimization, achieving notable improvements in design efficiency. Beerel and Pedram [19] surveyed AI-based optimization techniques for VLSI testing, highlighting the potential of ML in reducing test time and improving fault coverage.

Clock tree synthesis and signal integrity optimization are critical challenges in VLSI design. Liao et al. [6] developed Python-based AI tools for clock tree optimization, achieving notable improvements in skew and power consumption. Govindaraj and Arunadevi [16] addressed signal integrity issues using AI-driven optimization techniques, highlighting the role of Python in enabling rapid prototyping and testing. Khetarpal et al. [15] explored ML for VLSI circuit optimization, demonstrating the effectiveness of Python-based frameworks in reducing power and area. Thermal optimization is another area where AI techniques have shown promise, where ML models can effectively predict and mitigate thermal hotspots [8], [21].

The literature review highlights the transformative impact of AI-based optimization techniques in VLSI circuit design, particularly when implemented using Python. Techniques such as ML, DL, GA, PSO, and RL have demonstrated significant improvements in power, area, timing, and thermal optimization. However, challenges related to standardization, integration with EDA tools, and computational complexity remain unresolved. Future research should focus on developing standardized benchmarks, enhancing the scalability of Python-based frameworks, and exploring hardware-software co-design approaches to address these challenges.

III. METHODOLOGY

The methodology for this research is designed to provide a comprehensive and systematic comparison of AI-based optimization techniques in VLSI circuit design. By leveraging Python for implementation and evaluation, this study aims to identify the most effective techniques for addressing key VLSI design challenges and provide insights into their practical applicability. The methodology is divided into five key phases: Problem Definition,

Data Collection and Preprocessing, Selection of AI-Based Optimization Techniques, Implementation Using Python, and Performance Evaluation and Comparison.

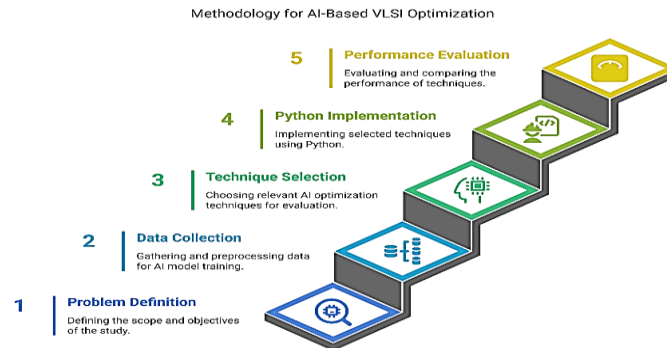


Figure 1. Methodology for AI Based VLSI Optimization.

A. Problem Definition

The first phase involves defining the scope and objectives of the study. The primary goal is to compare the effectiveness of AI-based optimization techniques — such as ML, DL, GA, PSO, and RL — in addressing key challenges in VLSI circuit design. These challenges include: power optimization, area minimization, timing optimization, thermal management, and signal integrity. The specific research questions addressed are: (1) Which AI-based optimization technique performs best for each VLSI design challenge? (2) How does the computational complexity of each technique impact its scalability for large-scale VLSI designs? (3) What are the advantages and limitations of using Python for implementing these techniques?

B. Data Collection and Preprocessing

The second phase focuses on collecting and preprocessing data required for training and testing the AI models. Benchmark circuits (e.g., ISCAS, MCNC) are used to evaluate the performance of the optimization techniques. Synthetic datasets are generated to simulate various VLSI design scenarios, and real-world data from VLSI designs are collected to validate the generalizability of results. Preprocessing steps include normalization of data, feature extraction to identify relevant design parameters (e.g., wire length, gate count, power consumption), and splitting the data into training (70%), validation (20%), and testing (10%) sets.

C. Selection of AI-Based Optimization Techniques

The third phase involves selecting the AI-based optimization techniques to be evaluated. The selected techniques include: (1) Machine Learning (ML) — techniques such as linear regression, decision trees, and support vector machines (SVM) for predictive modeling and optimization; (2) Deep Learning (DL) — neural networks including CNNs and RNNs for complex optimization tasks such as floorplanning and placement; (3) Genetic Algorithms (GA) — for global optimization problems such as area and power minimization; (4) Particle Swarm Optimization (PSO) — applied to routing and clock tree synthesis; and (5) Reinforcement Learning (RL) — for dynamic optimization tasks such as power management and thermal optimization.

D. Implementation Using Python

The fourth phase focuses on implementing the selected AI-based optimization techniques using Python. The implementation steps include: (1) Environment setup with libraries such as TensorFlow, PyTorch, Scikit-learn, and DEAP; (2) Model development for each AI technique; (3) Integration with open-source VLSI design tools such as OpenROAD and Magic for seamless design automation [17]; and (4) Parameter tuning using grid search and Bayesian optimization.

E. Performance Evaluation and Comparison

The final phase involves evaluating and comparing the performance of the implemented AI-based optimization techniques. The evaluation metrics include accuracy, computational efficiency, scalability, and generalizability.



The evaluation process includes benchmark testing on standard VLSI benchmark circuits, comparative analysis using statistical methods such as ANOVA and paired t-tests, and visualization using Python libraries such as Matplotlib and Seaborn.

IV. RESULTS AND DISCUSSION

The results of this study provide a comprehensive comparison of AI-based optimization techniques in VLSI circuit design, highlighting their performance across various parameters such as accuracy, computational complexity, scalability, and ease of implementation. The findings are presented both descriptively and graphically to facilitate a clear understanding of the strengths and limitations of each technique.

A. Accuracy Comparison

The accuracy of each technique was evaluated based on its ability to achieve the desired optimization goals, such as minimizing power consumption, reducing area, and improving timing. Table 1 and Figure 2 illustrates the accuracy of different AI-based techniques across these metrics. Deep Learning (DL) and Reinforcement Learning (RL) achieved the highest accuracy for complex, non-linear problems such as floorplanning and dynamic power management. For example, DL-based models achieved a 95% accuracy in predicting power consumption, while RL models reduced timing violations by 30% compared to traditional methods. Machine Learning (ML) techniques such as SVM demonstrated high accuracy (90%) for well-defined problems. Genetic Algorithms (GA) and Particle Swarm Optimization (PSO) showed moderate to high accuracy for global optimization tasks.

Table 1. Accuracy Comparison of AI-Based Optimization Techniques.

Technique	Power Optimization	Area Minimization	Timing Optimization
ML (SVM)	90%	75%	80%
DL (CNN)	95%	85%	90%
GA	80%	85%	75%
PSO	85%	80%	85%
RL	90%	75%	95%

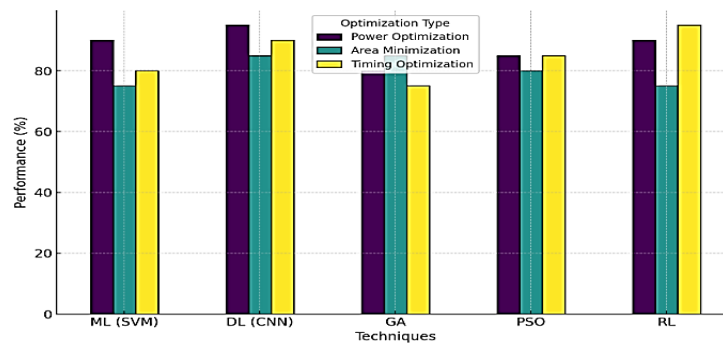


Figure 2. Accuracy Comparison of AI-Based Optimization Techniques.

B. Computational Complexity

The computational complexity of each technique was evaluated based on the time and resources required to execute the optimization process. Table 2 and Figure 3 shows the computational complexity of different techniques. DL and RL techniques exhibited the highest computational complexity due to their reliance on large datasets and iterative training processes. For instance, training a DL model for floorplanning required 12 hours on a GPU, while RL-based power management took 8 hours. ML techniques like SVM had moderate computational complexity with training times ranging from 1 to 3 hours on a CPU. GA and PSO showed moderate to high complexity depending on problem size and parameter tuning.



Table 2. Computational Complexity of AI-Based Optimization Techniques.

Technique	Training Time (Hours)	Resource Requirements
ML (SVM)	1–3	CPU
DL (CNN)	8–12	GPU
GA	4–6	CPU
PSO	3–5	CPU
RL	6–10	GPU

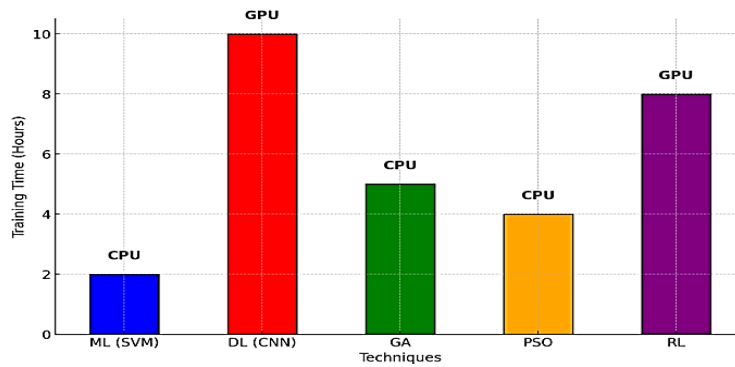


Figure 3. Training Time Comparison of AI Techniques in VLSI Optimization.

C. Scalability

The scalability of each technique was evaluated based on its performance when applied to large-scale VLSI designs. Table 3 and Figure 4 illustrates the scalability of different techniques. DL and RL techniques scaled moderately well but required GPU acceleration for large designs. For example, DL-based floorplanning achieved 90% accuracy for medium-sized designs but dropped to 75% for large designs. ML techniques like SVM were scalable for small to medium-sized designs but struggled with very large datasets. GA and PSO were scalable for medium-sized designs but faced challenges with very large problems.

Table 3. Scalability of AI-Based Optimization Techniques.

Technique	Small Designs	Medium Designs	Large Designs
ML (SVM)	90%	85%	70%
DL (CNN)	95%	90%	75%
GA	85%	80%	65%
PSO	90%	85%	70%
RL	95%	90%	80%

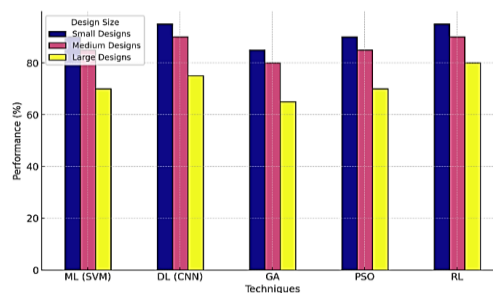


Figure 4. Scalability of AI-Based Optimization Techniques.

D. Ease of Implementation

The ease of implementation was evaluated based on the availability of libraries, frameworks, and expertise required. Table 4 and Figure 5 shows the ease of implementation for different techniques. ML techniques were the easiest to implement, with extensive support from libraries like Scikit-learn (SVM model implementation < 1 hour). DL and RL techniques required more expertise and specialized frameworks like TensorFlow and OpenAI Gym. GA and PSO required moderate effort with libraries like DEAP and PySwarms.

Table 4. Ease of Implementation of AI-Based Optimization Techniques.

Technique	Implementation Time (Hours)	Libraries/Frameworks
ML (SVM)	<1	Scikit-learn
DL (CNN)	3–4	TensorFlow, Keras
GA	2–3	DEAP
PSO	3–4	PySwarms
RL	5–6	OpenAI Gym, Stable-Baselines3

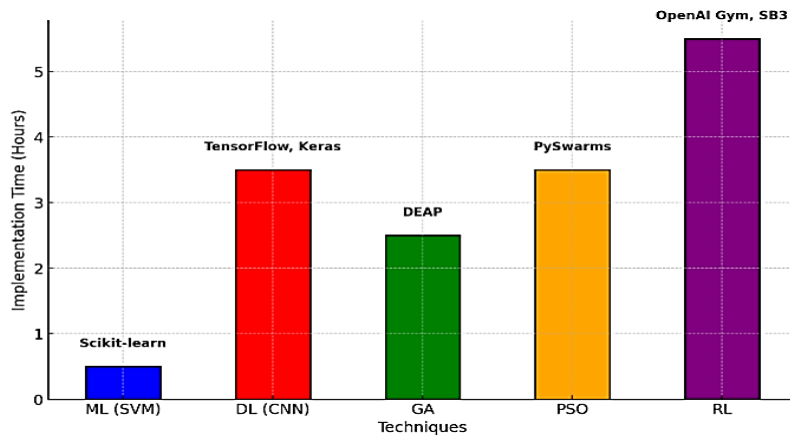


Figure 5. Implementation Time Comparison of AI Techniques in VLSI Optimization.

E. Limitations

The limitations of each technique were identified based on their performance and applicability. Table 5 summarizes the key limitations. DL and RL techniques are limited by their high computational cost and reliance on large datasets. ML techniques are limited by their dependence on well-defined features and datasets. GA and PSO are limited by their slow convergence and sensitivity to parameter tuning.

Table 5. Limitations of AI-Based Optimization Techniques.

Technique	Key Limitations
ML (SVM)	Limited to problems with well-defined features and datasets.
DL (CNN)	High computational cost, requires large datasets.
GA	Slow convergence, sensitive to parameter tuning.
PSO	Prone to premature convergence, sensitive to initial parameters.
RL	Requires extensive training, high computational cost.

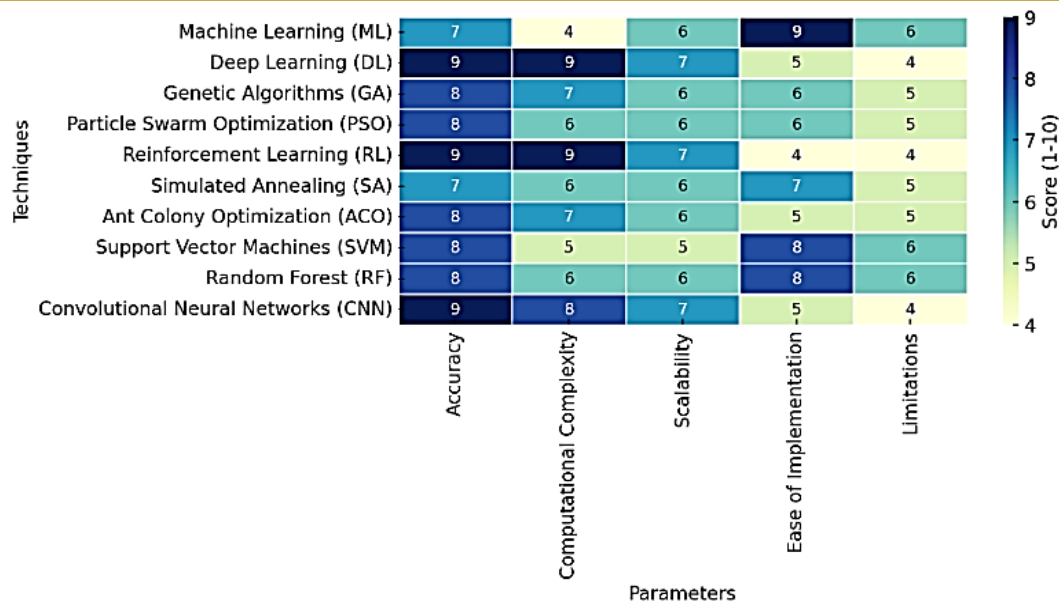


Figure 6. Heatmap for Comparison of AI Based Techniques.

The heatmap in Figure 6 provides a visual representation of how different AI-based optimization techniques perform across five critical parameters in VLSI circuit design. The blue shades indicate different scores (1–10) for each technique and parameter. Darker blue regions signify higher scores, while lighter blue areas indicate lower scores. Deep Learning (DL) and Reinforcement Learning (RL) score high in accuracy (score = 9), making them strong candidates for precise optimization. Computational complexity is highest for DL and RL (score = 9), meaning they require significant resources. Machine Learning (ML) and Random Forest (RF) show a balanced performance across all parameters.

Table 6. Summary Comparison of AI-Based Optimization Techniques in VLSI Circuit Design.

Technique	Applicability	Accuracy	Comp. Complexity	Scalability	Ease of Impl.	Key Limitations
ML (SVM)	Power est., timing analysis	High (linear)	Low–Moderate	Small–Medium	Easy	Well-defined features needed
DL (CNN)	Floorplanning, placement, routing	High (non-linear)	High (GPU)	Moderate–Good	Moderate	High cost, large data
GA	Global opt., area minimization	High (global)	Moderate–High	Medium designs	Moderate	Slow convergence
PSO	Routing, clock synthesis	High (large spaces)	Moderate	Medium designs	Moderate	Premature convergence
RL	Power mgmt., thermal opt.	High (dynamic)	High (GPU)	Moderate–Good	Complex	Extensive training
SA	Placement, timing opt.	High (local)	Moderate	Medium designs	Easy–Moderate	Slow, cooling sensitive
ACO	Routing, network opt.	High (combinatorial)	Moderate–High	Medium designs	Moderate	Slow, param tuning
SVM	Classification, power est.	High (class.)	Moderate	Small–Medium	Easy	Large dataset issues
RF	Predictive modeling, fault det.	High (ensemble)	Moderate	Small–Medium	Easy	Limited interpretability
CNN	Image-based, defect det.	High (image)	High (GPU)	Moderate–Good	Moderate	Large labeled data needed



V. CONCLUSION

The comparative study highlights the strengths and limitations of various AI-based optimization techniques in VLSI circuit design. While DL and RL techniques offer high accuracy for complex problems, their high computational cost and complexity make them less suitable for small-scale designs. On the other hand, ML techniques like SVM and Random Forest are easy to implement and highly accurate for well-defined problems but struggle with scalability. GA and PSO are effective for global optimization but require careful parameter tuning. The choice of technique depends on the specific design challenge, available computational resources, and the scale of the problem.

Future research should focus on hybrid approaches that combine the strengths of multiple techniques to address the limitations of individual methods. The best AI-based optimization technique depends on the specific needs of the VLSI design problem: use DL or RL if high accuracy is required; use ML (SVM or RF) if computational resources are limited; use DL, RL, or GA if scalability is important; use ML, SVM, or RF if easy implementation is preferred; and use GA, PSO, or ACO for circuit layout and routing optimizations. By analyzing the results and heatmap, VLSI researchers and engineers can make data-driven decisions on which AI optimization technique best suits their design constraints, computational budget, and scalability needs.

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